SCMVX008-01A

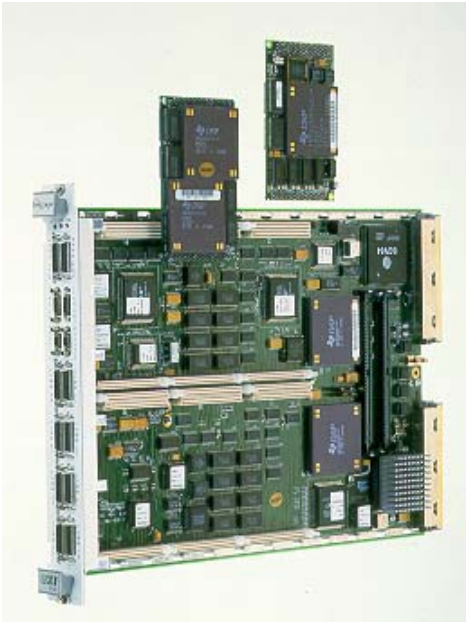
S E R V I C E N O T E

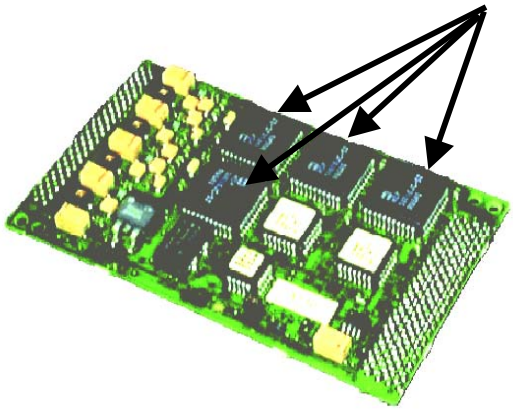
Supersedes:
SCMVX008-01

SCMVX008 Option 040 or VX008-66501
4 Channel DDC TIM Module (PAWN)

Serial Numbers: Any Harris chips with date codes less than: N0014, replace VX008-66501
Date Code format is: NYYWW (YY = Year WW = Week Number)

4 Harris chips can be found on the VX008-66501 DDC TIM Module:





ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:
INFORMATION ONLY
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PRINTED IN U.S.A.

Agilent Technologies

February 20, 2004

Descriptive title: SCMVX008 Option 040 High LO Feed thru - Spur at CF

To be performed by: Agilent-Qualified Personnel or Customer

Parts Required:

P/N	Description	Qty.
VX008-66501	4 CHANNEL DDC TIM Module	(1-6)

Situation:

Customer may complain of high LO feed thru (spur at CF) or about a steady tone when demodulating signals of interest. There are 4 Harris Chips used on the VX008-66501 DDC module. Earlier date codes of these Harris chips had a LO feed thru problem. Agilent compensated for this problem with software patch. Now Harris has fixed this LO feed thru spur and Agilent's software patch cause this LO feed thru spur to re-appear. Most customers do not care about LO feed thru because of the demodulation algorithm used. Only customers performing USB or LSB demodulation will notice this, so very few customers should report this as a problem.

Solution/Action:

If customer calls and complains of high LO feed thru spurs, spur at center frequency or a steady tone after USB/LSB demodulation. Replace customers VX008-66501 - 4 channel DDC TIM Module. Module replacement can be done by the customer or by an Agilent service center.

The customer may have to modify his code to remove the Agilent software patch that compensated for the original bad Harris chip.

Modify code as follows:

1. Search for the following variable in your C40 downloadable code: State.DcBuckout
2. Insert the following line of code above this line: State.DCBuckout = 0;

Example:

```
/* Shift buckout up, instead of shifting each data value down */
State.DCBuckout = 0;
offset = State.DcBuckout << 8;
```